

FIG. 2

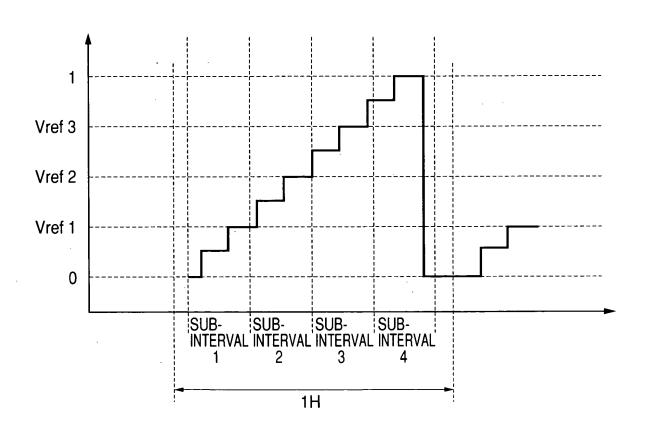
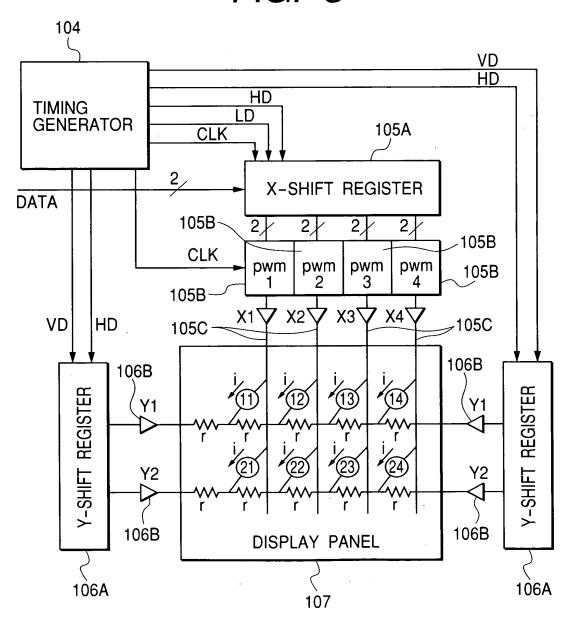
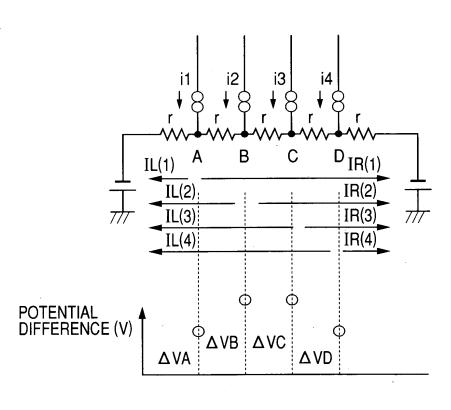


FIG. 3

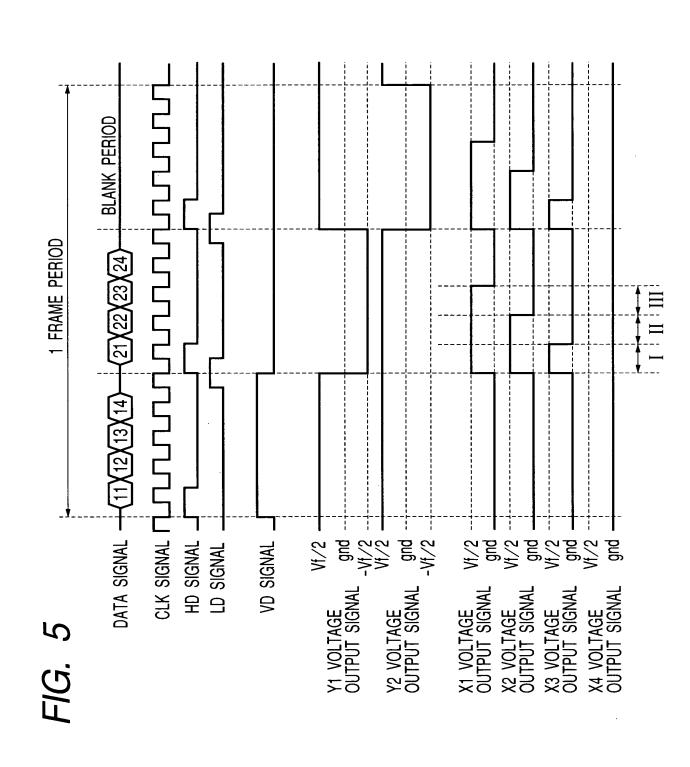




$$IL(1)=(r*4/5)*i1$$
  $IR(1)=(r*1/5)*i1$   $IL(2)=(r*3/5)*i2$   $IR(2)=(r*2/5)*i3$   $IL(3)=(r*2/5)*i3$   $IR(3)=(r*3/5)*i3$   $IL(4)=(r*1/5)*i4$   $IR(4)=(r*4/5)*i4$ 

$$\begin{split} & \Delta \, \text{VA} \! = \! r \! * \! \frac{4}{\Sigma} (\text{IL}(k)) \\ & \Delta \, \text{VB} \! = \! \Delta \, \text{VA} \! + \! r \! * \! ( \! \frac{4}{\Sigma} (\text{IL}(k)) \! - \! \frac{1}{\Sigma} (\text{IR}(k))) \\ & k \! = \! 2 \qquad k \! = \! 1 \end{split}$$
 
$$& \Delta \, \text{VC} \! = \! \Delta \, \text{VB} \! + \! r \! * \! ( \! \frac{4}{\Sigma} (\text{IL}(k)) \! - \! \frac{2}{\Sigma} (\text{IR}(k))) \\ & k \! = \! 3 \qquad k \! = \! 1 \end{split}$$
 
$$& \Delta \, \text{VD} \! = \! \Delta \, \text{VC} \! + \! r \! * \! ( \! \frac{4}{\Sigma} (\text{IL}(k)) \! - \! \frac{3}{\Sigma} (\text{IR}(k))) \\ & k \! = \! 4 \qquad k \! = \! 1 \end{split}$$





	PERIOD I	PERIOD II	PERIOD III
X1 VOLTAGE OUTPUT	NO	ON	NO
X2 VOLTAGE OUTPUT	NO	ON	OFF
X3 VOLTAGE OUTPUT	NO	OFF	OFF
X4 VOLTAGE OUTPUT	OFF	OFF	OFF

 $\equiv$ 

	PERIOD I	PERIOD II	PERIOD III	TOTAL OF ONE HORIZONTAL SCANNING INTERVAL
VOLTAGE DROP AT POINT A	ΔVA(I)	Δ VA(II)	Δ VA(III)	$(\Delta VA(I) + (\Delta VA(II) + (\Delta VA(III)) / 3$
VOLTAGE DROP AT POINT B	ΔVB(I)	$\Delta VB(I)$ $\Delta VB(II)$	Δ VB(III)	$\Delta VB(III)$ $(\Delta VB(I) + (\Delta VB(II) + (\Delta VB(III))/3$
VOLTAGE DROP AT POINT C	ΔVC(I)	Δ VC(II)	Δ VC(III)	$\Delta VC(II)$ $\Delta VC(III)$ $(\Delta VC(I) + (\Delta VC(II) + (\Delta VC(III))/3$
VOLTAGE DROP AT POINT D	ΔVD(I)	Δ VD(II)	Δ VD(III)	$\Delta VD(II) \mid \Delta VD(III) \mid (\Delta VD(I) + (\Delta VD(II) + (\Delta VD(III))/3$

(5)

FIG. 7

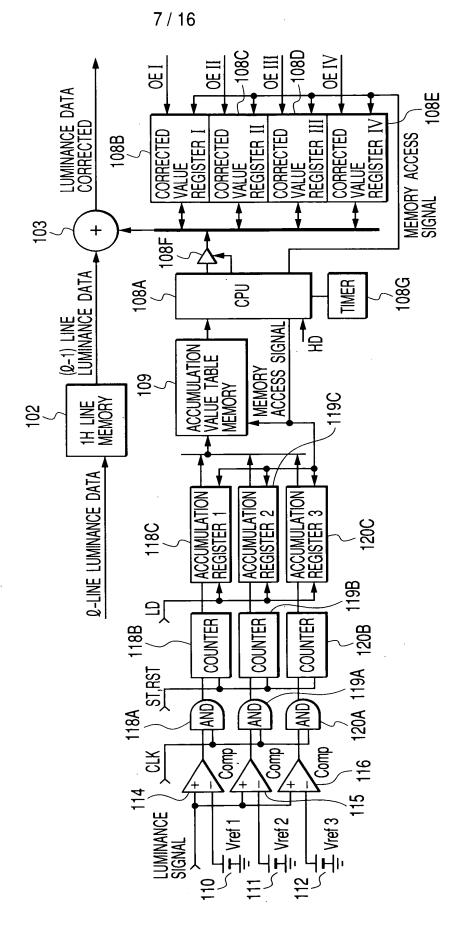


FIG. 8

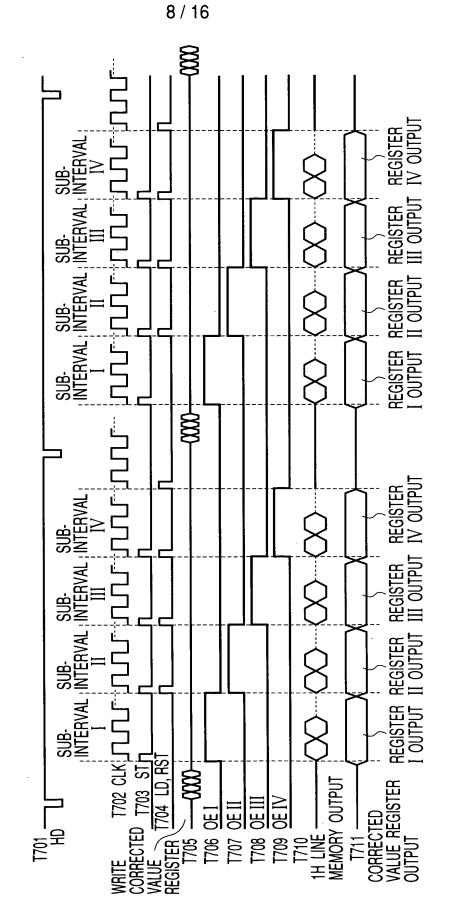
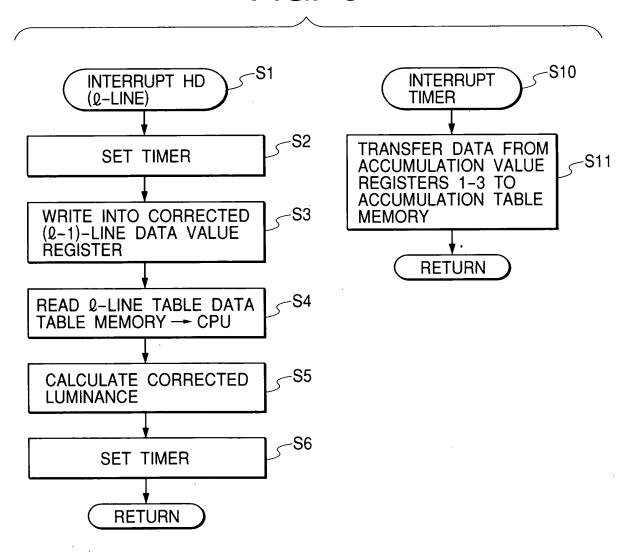
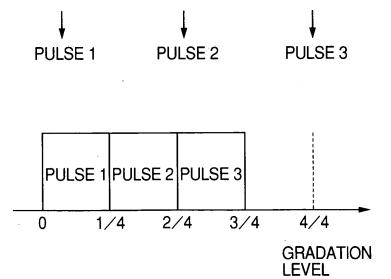
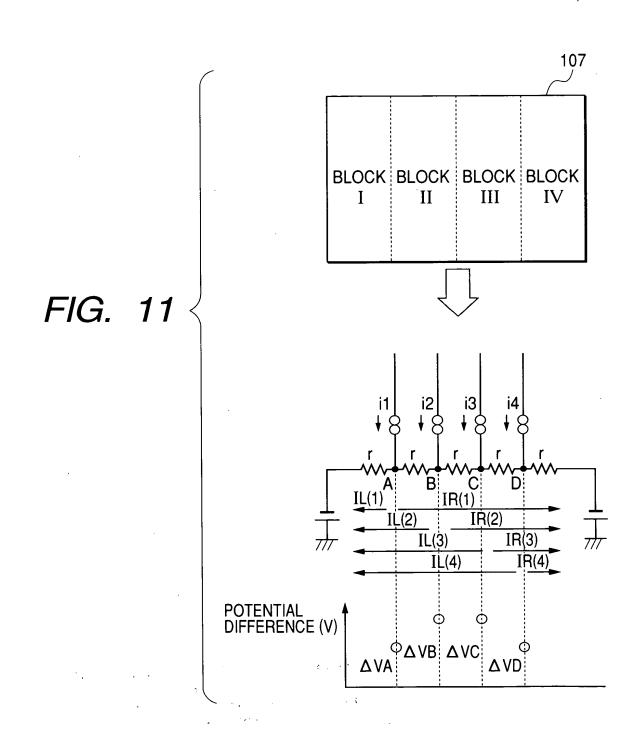


FIG. 9



	DATA'S GREATER THAN Vref 1	DATA'S GREATER THAN Vref 2	DATA'S GREATER THAN Vref 3
BLOCK I	NA 1	NA 2	NA 3
BLOCK II	NB 1	NB 2	NB 3
BLOCK III	NC 1	NC 2	NC 3
BLOCK IV	ND 1	ND 2	ND 3





-108B -108D -108C Œ IV OE III OE II **E**I 108E CORRECTION DATA (Q-1) LINE LUMINANCE DATA CORRECTION - VALUE REGISTER IV CORRECTION VALUE REGISTER I CORRECTION VALUE REGISTER III CORRECTION VALUE REGISTER II MEMORY ACCESS SIGNAL 108F 108G TIMER 108A <u>8</u> MEMORY ACCESS SIGNAL Î<sub>全</sub> 69 1H LINE MEMORY -119C Q-LINE LUMINANCE DATA ACCUMULATION -REGISTER 3 ACCUMULATION REGISTER 2 ACCUMULATION REGISTER 1 120C 118C 138 의 COUNTER COUNTER COUNTER 115 116 120A 119A 120B 118B ST, RST (QNE AND AND AND AND 당 Comp Somo Comp LUMINANCE SIGNAL ñ Vref 2 人上 上 Vref 3 I Vref 1

12/16

FIG. 13

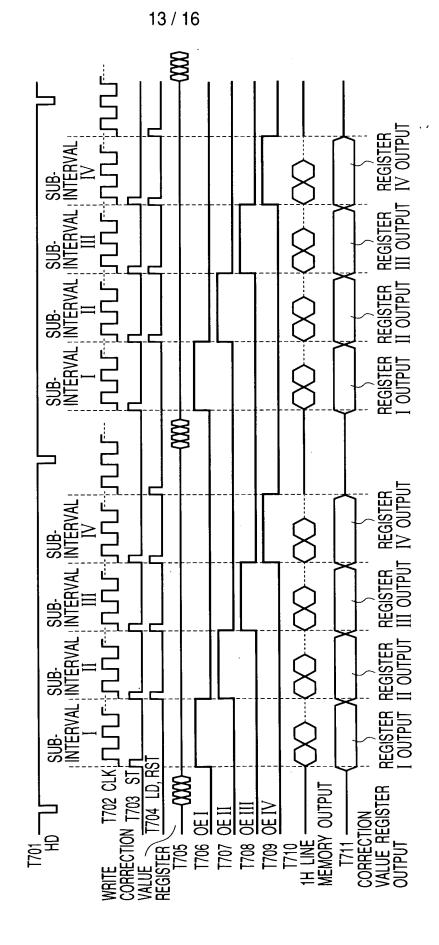


FIG. 14

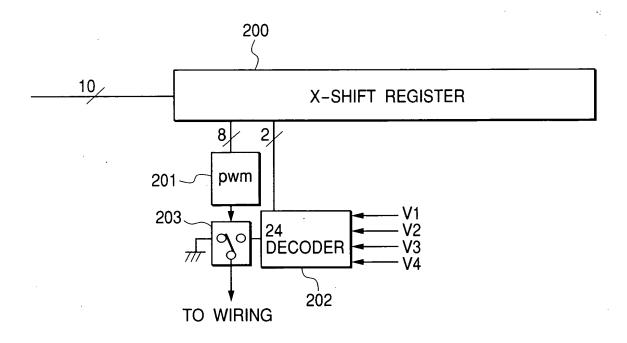


FIG. 15

CORRECTION DATA	OUTPUT	
00B	V1	
01B	V2	
10B	V3	
11B	V4	

UNIT DRIVING ROW LINE 302D BLOCK II | BLOCK III | BLOCK IV UNIT DRIVING COLUMN LINE 302C DISPLAY PANEL UNIT (m×n PIXELS) VARIABLE 302B VARIABLE POWER SOURCE 1 707 VARIABLE **BLOCK I** 8 302A~ UNIT DRIVING ROW LINE UNIT CALCULATING CORRECTION QUANTITY TIMING GENERATOR 1H LINE MEMORY TABLE MEMORY INTEGRATOR NTEGRATOR 8 102 COMPARATOR 3 COMPARATOR COMPARATOR PROCESSING IMAGE SIGNAL  $\overline{2}$ \\_\_\_ Vref 2 \\_ \_HVref3

15/16

